

WHAT IS CLAIMED IS:

1. A wireless and passive tableting apparatus for computer inputting comprising a tablet and a pen, characterized in that nothing wires the pen and the
5 tablet and no battery is in the pen, the tablet which can sense pressure from the pen comprises a transmitting circuit, a receiving circuit, an amplifying circuit, a phase angle and amplitude detecting circuit and an integrating circuit; and the pen comprises a paralleled resonant circuit composed of capacitors and inductors; the connection relations between them are as follows: an auxiliary CPU, which
10 generates a square wave, connects with the transmitting circuit, which can transmit electromagnetic wave continuously; the pen circuit receives the electromagnetic wave transmitted from the transmitting circuit to produce a resonant signal; the resonant signal is transmitted to the receiving circuit continuously, and amplified by an amplifying circuit that connects with the receiving circuit; the amplified signals is
15 inputted into the phase angle and amplitude detecting circuit, and the signals output from the phase angle and amplitude detecting circuit are inputted into a primary CPU via the integrating circuit.

2. The wireless and passive tableting apparatus of claim 1, wherein the
20 transmitting circuit and receiving circuit comprise the coils in the direction Y, coils in the direction X and chips; the RX+ terminals of the receiving circuit are connected to pins 3 corresponding to port X of chips L10, L11, L12, L13, L14 and L15; for chips L10, L11 and L12, their X0~X7 ports corresponding to pins 13, 14, 15, 12, 1, 5, 2 and 4 connect with the coils in the direction of Y respectively, the output
25 terminals of coils are grounded, INH terminals corresponding to pins 6 are used for chip selection, A terminal, B terminal and C terminal corresponding to pins 11, 10 and 9 are gating terminals, all connecting with the primary CPU, VEE terminal corresponding to pin 7 is connected to a negative voltage; the square wave generated by the auxiliary CPU is inputted into pin 3 corresponding to port X of
30 chips L13, L14, and L15 via the TX+ terminals of the transmitting circuit; for chips

L13, L14, and L15, their X0~X7 ports corresponding to pins 13, 14, 15, 12, 1, 5, 2 and 4 connect with the coils in the direction of Y respectively, the output terminals of coils are grounded, INH terminals corresponding to pins 6 are used for chip selection, A terminal, B terminal and C terminal corresponding to pins 11, 10 and 9 are gating terminals, all connecting with the primary CPU, VEE terminal corresponding to pin 7 is connected to a negative voltage.

3. The wireless and passive tableting apparatus of claim 1, wherein the connection relations of the amplifying circuit in the tablet are as follows: the RX+ terminal receiving signals connects with ends of two parallel resistors R1 and R2, the other end of the resistor R2 connects with pin 2 of an amplifier IC12A and one end of a resistor R3 in parallel, and the other end of the resistor R3 connects with pin 1 of IC12A, pin 12 of the chip IC14 and one end of a resistor 6; the pin 4 of the IC12A is connected to analogue ground; the other end of resistor R1 connects with one end of a resistor R4, a capacitor C6 and a reference voltage terminal; the other end of the resistor R4 connects with pin 3 of the chip IC12A, the other end of the capacitor C6 is connected to analogue ground and one end of a capacitor C7, the other end of the capacitor C7 connects with the pin 8 of the chip IC12A and power supply VDD; the other end of the resistor R6 connects with pin 13 of chip IC14 and one end of a resistor R7; the other end of the resistor R7 connects with the pin 14 of chip IC14 and one end of a resistor R8; the other end of the resistor R8 connects with pin 15 of chip IC14 and one end of the resistor R9; the other end of the resistor R9 connects with pin 1 of chip IC14 and one end of a resistor R10; the other end of the resistor R10 connects with pin 2 of chip IC14 and one end of a resistor R11; the other end of the resistor R11 connects with pin 4 of chip IC14 and one end of a resistor R12; the other end of the resistor R12 connects with pin 5 of chip IC14 and one end of a resistor R13; the other end of the resistor R13 connects with a reference voltage terminal; the pin 3 of chip IC14 connects with one end of a capacitor C1, the other end of the capacitor C1 connects with one end of resistor R16 and the pin 5 of chip IC12B; the other end of the resistor R16 connects with

the reference voltage terminal; the pin 7 of chip IC12B, which outputs the output signals, connects with one end of a resistor R26; the other end of the resistor R26 connects with pin 6 of chip IC12B and one end of a resistor R23; the other end of the resistor R23 connects with the reference voltage terminal; the pin 11 of chip
5 IC14 connects with a signal GA, and pin 10 of chip IC14 connects with a signal GB and pin 9 of chip IC14 connects with a signal GC, and pin 16 of chip IC14 connects with a power supply VDD and one end of a capacitor C14; the other end of the capacitor C14 connects with the analogue ground, and so do pin 6, pin 7 and pin 8 of chip IC14.

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4. The wireless and passive tableting apparatus of claim 1, wherein, the connection relations of the phase angle and amplitude detecting circuit are as follows: an IN terminal connects with pin 3 of chip IC9A and one end of a resistor R17 in parallel, the other end of the resistor R17 connects with pin 6 of chip IC9B
15 and one end of a resistor R18 in parallel, the other end of the resistor R18 connects with pin 7 of chip IC9B and pin 4 of chip IC8B in parallel; pin 5 of chip IC9B connects with one end of a resistor R19, the other end of the resistor R19 connects with a reference voltage; pin 1 of chip IC9A connects with pin 2 of chip IC9A and pin 8 of chip IC8C; pin 8 of chip IC9A is connected to a power supply VDD, and pin
20 4 of chip IC9A connects with an analogue ground; pin 5 of chip IC8B connects with pin 2 of MCU2; pin 6 of chip IC8C connects with pin 3 of MCU2; pin 3 of chip IC8B and pin 9 of chip IC8C are connected together, used as an output terminal; pin 11 of MCU2 connects with ends of a capacitor C4 and a resistor R28 in parallel, wherein two other ends of the capacitor C4 and resistor R28 are connected
25 together to connect with a base of a triode Q1, whose emitter connects with one end of a capacitor C3 in series; the other end of the capacitor C3 connects with one end of a resistor R29 and TX- in parallel; the other end of a resistor R29 connects with VEE, a collector of the triode Q1 connects with TX+ and one end of a capacitor C2 in parallel; and the other end of the capacitor C2 connects with TX-;
30 pin 5 of MCU2 connects with an OSC clock, and pin 1 of MCU2 connects with ends

of a resistor R25 and a capacitor C5 in parallel; the other end of the resistor R25 connects with a power supply VCC, and the other end of the capacitor C5 is grounded; hang up such pins of MCU2 as pin 4, pin 6, pin 7, pin 8, pin 9, pin 12, pin 13 and pin 14; but let its pin 15 connect with DONE, its pin 16 connect with
5 CMD0, its pin 17 connect with CMD1, its pin 18 connect with CMD2, its pin 19 connect with CMD3, and its pin 20 connect with VCC and one end of a capacitor C19 in parallel; the other end of the capacitor C19 connects with pin 10 of MCU2 and the ground in parallel.

10 5. The wireless and passive tablet of claims 1, wherein the connection relations of said integrating circuit are as follows: an IN terminal connects with one end of resistor R21 in series; the other end of the resistor R21 connects with pin 2 of chip IC10A, pin 11 of IC8D and one end of a capacitor C21 in parallel; the other
15 end of the capacitor C21 connects with pin 10 of chip IC8D and Pin 1 of chip IC10A in parallel; pin 12 of chip IC8D connects with the primary CPU; and for chip IC10A, its pin 3 connects with a reference voltage, pin 4 connects with an analogue ground, pin 8 connects with a power supply VDD, and pin 1 connects with one end of a resistor R20, the other end of the resistor R20 is used as an output terminal and connected with the primary CPU.

20 6. The wireless and passive tableting apparatus of claim 5, wherein, the input terminal of the integrating circuit is connected to OUT terminal of the phase angle and amplitude detecting circuit, and generates two sets of signals of I phase and J phase having a phase difference of 90 degree.

25 7. The wireless and passive tableting apparatus of claim 1, wherein, the connection relations of the paralleled resonant circuit in the pen circuit are as follows: an inductor L1 connects with a variable capacitor C1 and the capacitors C2, C3, C4, C5, C6 and C7 in parallel; the last two ends of the circuit connect with one
30 end of a switch K1 and one end of a resistor R1 in series respectively, and the

other end of the switch K1 connects with the other end of the resistor R1, to form a loop.

8. The wireless and passive tableting apparatus of claim 1, wherein, a switch
5 K1 of the pen is a switch on the pen, functioning as the right button of a mouse.